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	Applicant Chien-Chao Huang et al.	
	Filing Date 07/25/2003	Confirmation No. 2641

U. S. PATENT DOCUMENTS

Examiner Initial	Cite No.	Document No.	Kind Code (if known)	Name	Date	Class	Subclass

FOREIGN PATENT DOCUMENTS

Examiner Initial	Cite No.	Country Number Code	Kind Code	Publication Date	Name of Patentee or Applicant	Page, Columns, Lines where Relevant Passages Appeal	T

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initial	Cite No.	(Include name of author in CAPITAL LETTERS, Title of the Article (when appropriate) title of the item (book, magazine, journal, symposium, catalog, etc.) date, pages, volume, issue number(s), publisher, city and/or country where published)	T
Dh	1	Ootsuka, F., et al., "A Highly Dense, High-Performance 130nm node CMOS Technology for Large Scale System-on-a-Chip Applications", IDEM 00, IEEE 0-7803-6438, (APR 2000), pp. 575-578	
Dh	2	Tiwari, S., et al. "Hole Mobility Improvement in Silicon-on-Insulator and Bulk Silicon Transistors Using Local Strain", IDEM 97, (APR 2000), pp. 939-941	

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